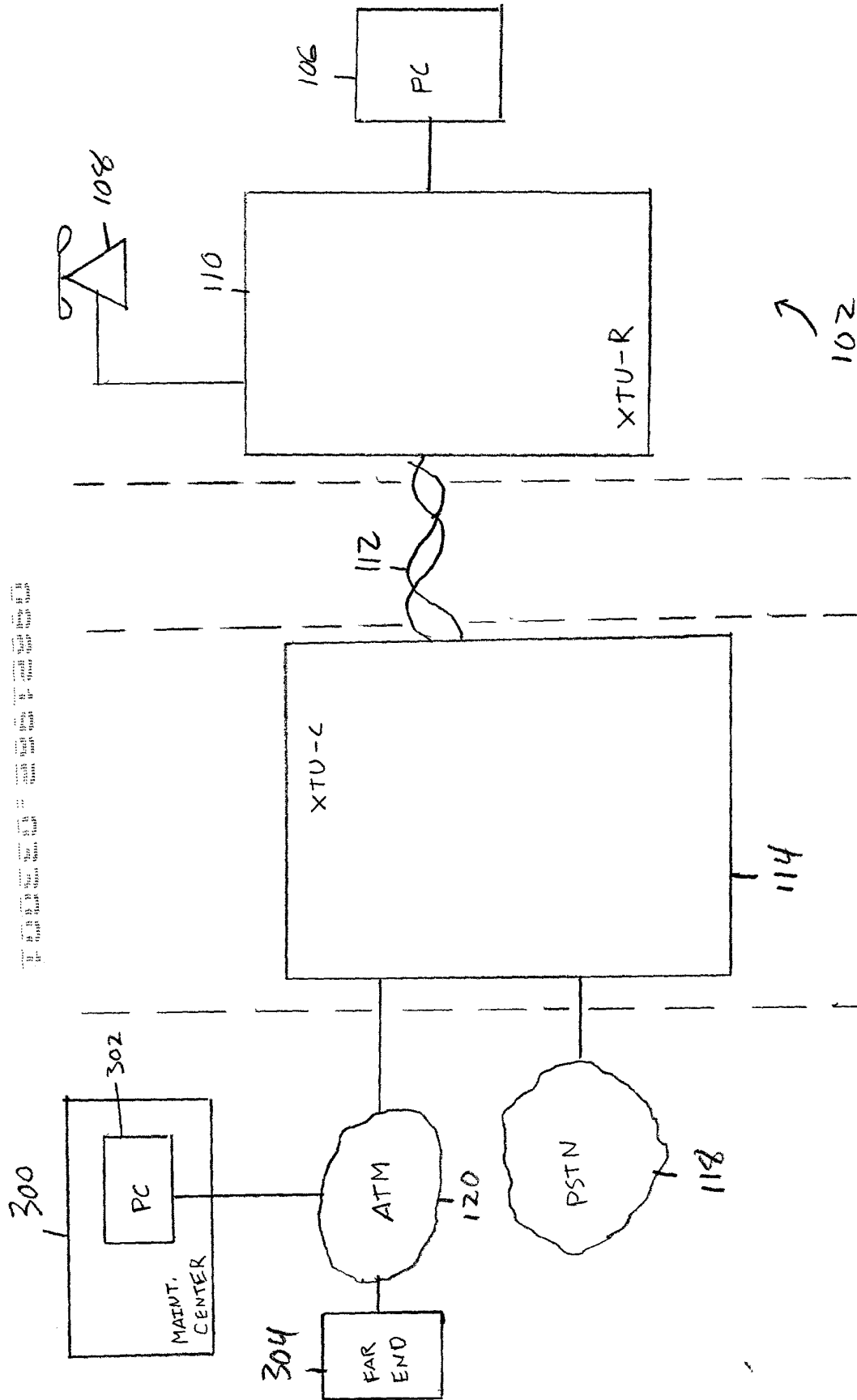


FIG. 1





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FIG. 4 is a block diagram of a system 114 for processing signals. The system 114 includes a Controller 218, a USV DSP 220, a USV D/A 222, a USV BLF 224, a xDSL DSP 226, a xDSL D/A 228, a xDSL BLF 230, a USV Battery Feed/Line Interface Transformer 402, a xDSL Line Driver and Transformer 404, a USV DSP 208, a USV A/D 206, a USV BLF 204, a xDSL DSP 216, a xDSL A/D 214, a xDSL BLF 212, a LPF 202, and a HPF 210. The system 114 is connected to a network 112.

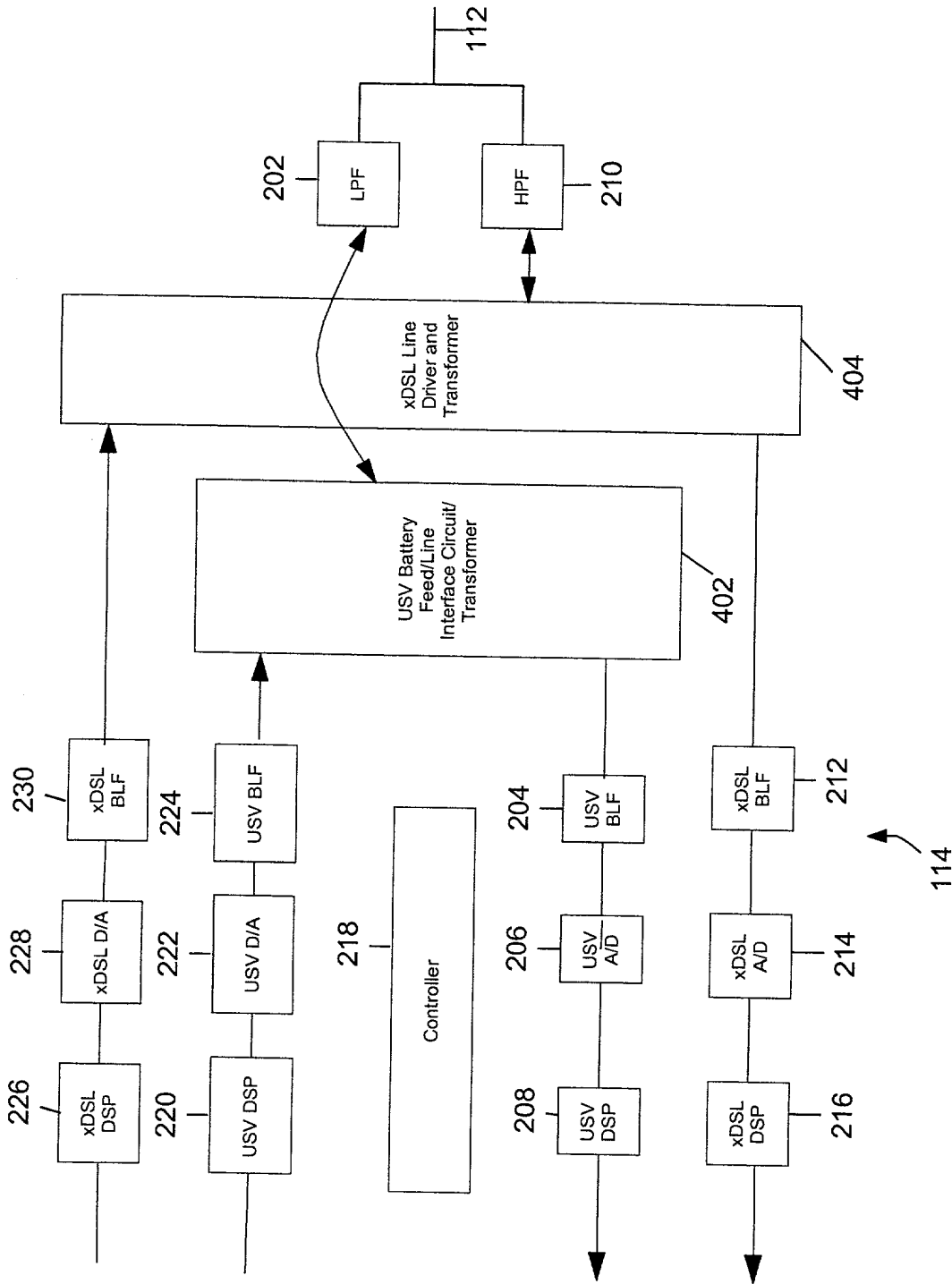


FIG. 4

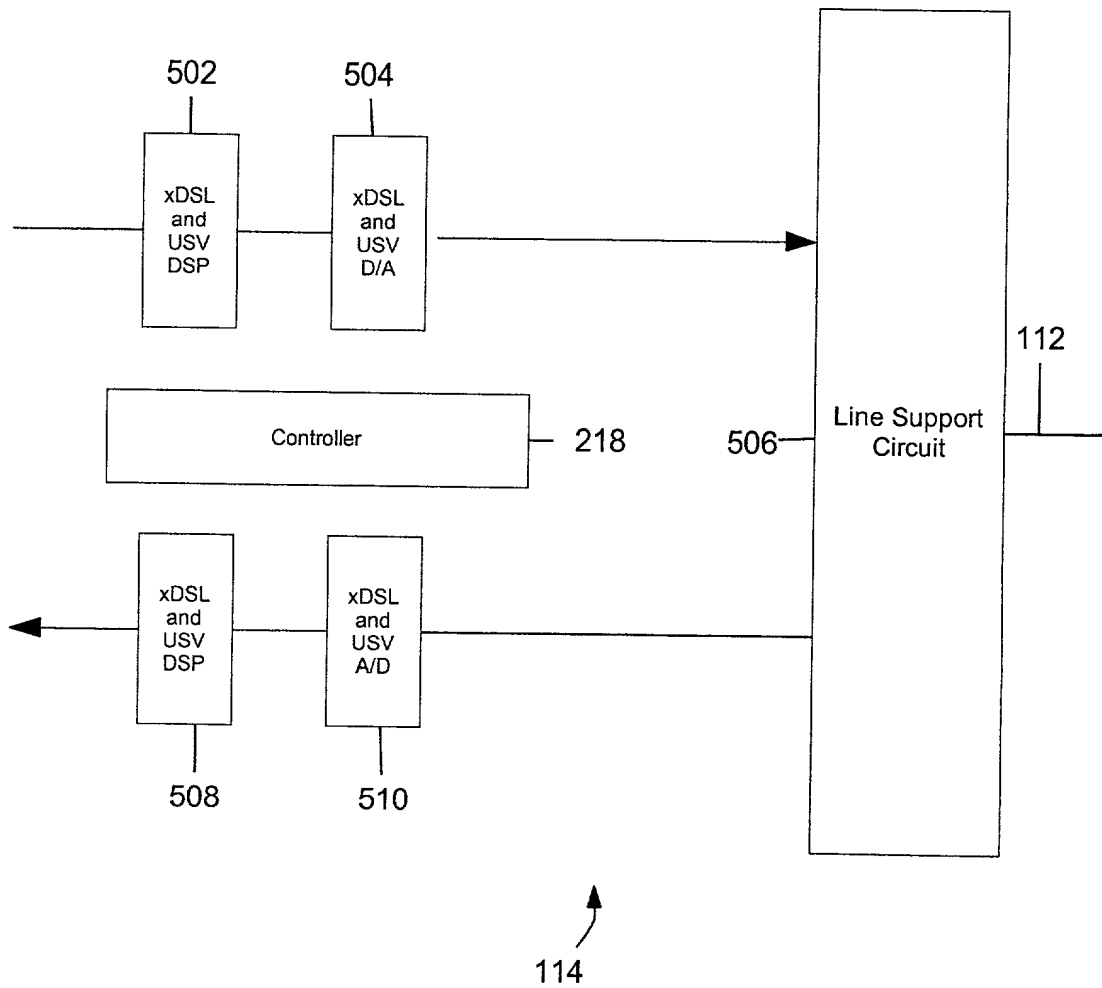


FIG. 5